



PTO/SB/122 (04-05)

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Application Number	10/665,927
Filing Date	September 17, 2003
First Named Inventor	Alexander Tetelbaum
Art Unit	2825
Examiner Name	Doan, Nghia M.
Attorney Docket Number	02-5799 / 81604

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Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

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Signature

Typed or Printed
Name

Timothy Croll, Reg. No. 36,771

Date

20 APR 06

Telephone

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NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

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PTO/SB/96 (08-03)

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: LSI Logic CorporationApplication No./Patent No.: 10/665,927 Filed/Issue Date: September 17, 2003Entitled: Method of Noise Analysis And Correction Of Noise Violations For An Integrated Circuit DesignLSI LOGIC CORPORATIONa CORPORATION

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or2. ☐ an assignee of less than the entire right, title and interest.The extent (by percentage) of its ownership interest is _____ %
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B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

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[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

20 APR 06
Date

(408) 433-7191

Telephone number

Timothy Croll, Reg. No. 36,771

Typed or printed name


SignatureSr. Corporate Counsel - IP

Title

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